

# National Institute of Technology, Tiruchirappalli: Performa for CV of Faculty/ Staff Members

## Curriculum Vitae



### Brief Profile:

*“The only true teacher is he who can immediately come down to the level of the student, and transfer his soul to the student’s soul and see through and understand through his mind. Such a teacher can really teach none else”* quoted by Swami Vivekananda. I strongly believe in making the student to understand, apply and develop the concepts as applications. My studies reflect my interest in the area of Electronics, as my bachelor’s degree is in Electrical and Electronics Engineering. Subsequently, I pursued a Master’s degree in Applied Electronics to narrow down my interest in Electronics Engineering. This gave me an opportunity to have an excellent exposure towards the digital design and VLSI circuits related courses. My knowledge thirst made me go on and register for a Doctoral degree in VLSI design at the Department of Electronics and Communication Engineering of **College of Engineering, Anna University, Chennai**, a world-class university in India. My research work at this institution made me realize how pertinent this field of engineering is to real world applications. Rising from humble beginnings, I believe that my focus to achieve has brought me a long way. After over 14 years of research experience, I am still motivated to look for newer opportunities to realize my goals.

1. Name : **S. Moorthi**
2. Designation : **Associate Professor**
3. Office Address: **Dept. of EEE, NIT Trichy - 620015**
4. Telephone (Direct) (Optional) : **0431-2503267**
5. Email (Primary): **srimoorthi@nitt.edu** Email  
(Secondary) :
6. Field(s) of Specialization : **VLSI and Embedded Systems**

### 7. Employment Profile

Job Title	Employer	From	To
Lecturer	Bannari Amman Institute of Technology, Sathyamangalam.	Feb. 2003	June 2004
Lecturer	Madras Institute of Technology, Anna University, Chennai-44	July 2004	July 2007
Assistant Professor	National Institute of Technology, Tiruchirappalli-15	July 2007	March 2018
Associate Professor	National Institute of Technology, Tiruchirappalli-15	March 2018	Till date

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### 8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
Ph.D	Anna University, Chennai	2008	--	VLSI Systems
M.E.,	PSG College of Technology, Coimbatore	2003	First	Applied Electronics
B.E.,	University of Madras	2001	First	EEE

### 9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	To
First Year Coordinator	Institution	September 2016	September 2018
Hostel Warden – Zircon	Institution	2011	2015

### 10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	To

### 11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization
2018	Faculty Award for Sponsored Projects	NIT Trichy

### 12. Fellowships

Year of Award	Name of the Fellowship	Awarding Organization	From (Month/Year)	To (Month/Year)
2011	Post-Doctoral Fellowship	Royal Institute of Technology, Sweden	May/ 2010	March/ 2011

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### 13. Details of Academic Work

#### (i) Curriculum Development:

- Involved in the development of curriculum and syllabi for B.E. (ECE), M.E. Electronics and M.E. Communication and Networking at Madras Institute of Technology, Chennai.
- Involved in the development of curriculum and syllabi for B.Tech. (EEE) and M.Tech. (Power Electronics and Power Systems) at National Institute of Technology, Tiruchirappalli.

#### (ii) Courses taught at Postgraduate and Undergraduate levels:

- Digital Electronics
- Electron Devices
- Digital Signal Processing
- Measurements and Instrumentation
- Applied Electronics
- Energy and Environment Engineering
- VLSI Design
- Embedded System Design
- Digital System Design and HDLs

#### (iii) Projects guided at Postgraduate level: 25

#### (iv) Other contribution(s)

### 14. Details of Major R&D Projects

Title of Project	Funding Agency	Duration		Status
		From	To	Ongoing/ Completed
Development of Wireless Sensor network for online monitoring and control in smart micro-grid applications	DST – SYST	2018	2021	Ongoing
A Novel Hybrid Energy System for Supplying Isolated Loads with FPGA Based Energy Management Scheme	National Institute of Wind Energy, MNRE, Govt. of India.	2013	2017	Completed
Development of Micro-Grid Controller Integrated with Multiple Types of Renewable Sources and Loads	Power Grid Corporation of India Ltd., Govt. of India.	2013	2014	Completed
Design and Analysis of Phase Locked Loop Architectures Used as High Speed Clock Generator for System On-Chip Applications	TEQIP – World bank (under Networking – Joint Research)	2007	2009	Completed

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15. Number of PhDs guided

Name of the PhD Scholar	Title of PhD Thesis	Role (Supervisor/ Co-Supervisor)	Year of Award
Vijaykumar K.	Control Strategy for WRIG system with certain power electronic converter topologies for effective utilization of wind energy	Co-supervisor	2013
Vasumathi B	Improved neural network based optimization algorithms for harmonic estimation	Supervisor	2014
Venkatraman K	Design and Development of FPGA Based Embedded System for Online Monitoring, Power Management and Control of Power Quality Conditioners in a Micro-grid	Supervisor	2016
Dheepanchakkravarthy A	FPGA Controlled FL-DSTATCOM for Multifarious Load Compensation in Power Distribution System	Supervisor	2019

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
21-03-2003 to 27-03-2003	AICTE-ISTE sponsored short term course on "Energy Efficient Electrical Systems"	National	Participant	Dept of EEE, NSS college of Engineering, Palakkad, Kerala	Dept of EEE
05-05-2003 to 17-05-2003	AICTE Sponsored Faculty development programme on "Innovative Teaching Techniques"	National	Participant	Dept of Mechanical Engineering, Bannariamman Institute of Technology, Sathyamangalam.	Dept of Mech. Engg.
24-11-2003 to 29-11-2003	CFD Anna University, Chennai sponsored Course on "Embedded Architecture"	National	Participant	Kongu Engineering College, Erode.	KEC

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21-01-2004 to 28-01-2004	Short Evening course on “Computer Aided design of Electronic Circuits” – organized and Participated.	National	Participant	Dept of ECE, Bannariamman Institute of Technology, Sathyamangalam.	Dept of ECE
07-02-2004 & 08-02-2004	An Intensive workshop on “Fiber Optic Communication”	National	Participant	Dept of ECE, PSG College of Technology, Coimbatore and Benchmark, Chennai.	Dept of ECE
06-12-2004 to 10-12-2004	CFD Anna University, Chennai sponsored course on “Principles of Environmental Science and Engg.”	National	Participant	MIT Campus of Anna University, Chennai.	MIT
13-06-2005 to 18-06-2005	CFD Anna University, Chennai sponsored course on “Digital Signal processing”	National	Participant	CEG Campus of Anna University, Chennai.	CEG, AU
27-03-2006 to 29-03-2006	Training on “CADENCE Design Tools”	National	Participant	Cadence Design Systems, Bangalore.	Banga lore
19-06-2006 to 24-06-2006	Continuing Education Programme on “Embedded Systems and Technology	National	Participant	GSSST, IIT, Kharagpur.	IIT, Khara gpur
26-01-2007 & 27-01-2007	Two-day “Workshop on Electromagnetic Theory Revisited”	National	Participant	Dept of Electronics Engg., MIT Campus of Anna University, Chennai.	Dept of ECE
26-01-2007 & 27-01-2007	Two-day “Workshop on Electromagnetic Theory Revisited”	National	Participant	Dept of Electronics Engg., MIT Campus of Anna University, Chennai	MIT
07-04-2007	One day Workshop on “Emerging Trends in VLSI and ASIC Designs”	National	Participant	Dept of ECE, CEG Campus of Anna University, Chennai.	Dept of ECE, CEG
23-05-2007 & 24-05-2007	Two Day training programme on “ARM processor-based development system” – organized and participated.	National	Participant	Dept of Electronics Engg., MIT Campus of Anna University, Chennai and Embed system solutions,Chennai	MIT

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04-06-2007 to 15-06-2007	Two week course on “Analog VLSI Design”	National	Participant	Prof. Radhakrishna Rao foundation and Dept of ECE, TCE, Madurai.	Dept of ECE, TCE
17-08-2007 & 18-08-2007	Two day Wokshop on “An Overview of VLSI-CAD Tools”	Local	Participant	ECE & CSE depts of NIT, Trichy	NIT-T
20-08-2007 to 27-08-2007	Workshop on “Wind power Integration”	Local	Participant	Dept of EEE, NITT	NIT-T
28-08-2007 & 29-08-2007	Workshop on “Recent trends in Wind power generation and FACTS”	Local	Participant	Dept of EEE, NITT	NIT-T
28-09-2007 & 29-09-2007	Workshop on “ Electronic Circuit Design techniques” – organized and participated.	Local	Participant	Dept of EEE, NITT	NIT-T
3-12-2007 to 7-12-2007	An Interaction programme “System design and Analog Interfacing”	National	Participant	Texas Instruments, Bangalore.	TI
07-01-2008 & 08-01-2008	Workshop on “Embedded Systems Development on FPGAs”	National	Participant	CG-CoreEl solutions, and Dept of Electronics Engg., MIT Campus of Anna University, Chennai.	MIT
28-01-2008 to 01-02-2008	AICTE – QIP Sponsored short term course on “Electrical Machines and Power Electronics in Renewable Energy Systems”	National	Participant	Dept of EEE, NITT	NIT-T
16-06-2008 to 28-06-2008	MHRD-AICTE Sponsored Summer school on “ Power Electronics and renewable Energy Electric Conversion Systems”	National	Participant	Dept of EEE, NITT	NIT-T
07-07-2008	One day Tutorial on “ Analog VLSI filters and Mixed signal Design”	National	Participant	ECIL, Bangalore.	ECIL
08-08-2008 & 09-08-2008	Two day Workshop on “ Right to Information ACT 2005 and Role of Information Officer”	National	Participant	National Archieves of India and NITT	NIT-T

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21-08-2008 to 27-08-2008	One week Workshop on “ Instructional Design and Delivery system”	National	Participant	NITTTR, Govt of India, MHRD, Chennai	NIT-T
12-11-2008 to 14-11-2008	National Workshop on Power Electronics	National	Participant	NaMPET, CDAC & NITT	NIT-T
02-03-2009 & 03-03-2009	A course on “Sigma-Delta Converters”	Internati onal	Participant	FTD, Singapore	FTD
04-03-2009 & 05-03-2009	A course on “ Low voltage Analog design in CMOS scaled Technology”	Internati onal	Participant	FTD, Singapore	FTD
04-05-2009	One day Workshop on “ Power Electronic Simulation – SEQUEL”	National	Participant	NaMPET & NITT	NIT-T
01-06-2009 to 05-06-2009	A course on “Advanced DSP Design Techniques”	National	Participant	IIT, Kharagpur	IIT

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/  
Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (Internatio nal/ National/ Local)	Date (s)	Role	Venue
Short course on “Digital controllers for Power Applications”	National	03-11-2017 to 04-11-2017	Convenor	Dept. of EEE, NIT-T
Two-week ISTE Workshop on “CMOS Mixed-signal RF VLSI Design”	National	30-01-2017 to 04-02-2017	Convenor	Dept. of EEE, NIT-T
SC on “Embedded System Design using FPGA Board”.	National	23-04-2016 to 24-04-2016	Convenor	Dept. of EEE, NIT-T
STC on “Role of Power Electronics in Power Engineering”	National	04-03-2016 & 05-03-2016	Convenor	Dept. of EEE, NIT-T
Short-term Course on “Real time Controller Design using C2000 Microcontroller”	National	26-09-2014 to 28-09-2014	Convenor	Dept. of EEE, NIT-T
STC on “Low power Controller design using MSP 430 Microcontroller”	National	20-06-2014 & 21-06-2014	Convenor	Dept. of EEE, NIT-T

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Two-week ISTE Workshop on “Analog Electronics”	National	04-06-2013 to 14-06-2014	Convenor	Dept. of EEE, NIT-T
Short-term course on “MSP 430 Microcontroller based system design”	National	23-02-2013 & 24-02-2013	Convenor	Dept. of EEE, NIT-T
Short-term course on “FPGA Based System Design”	National	13-07-2012 & 14-07-2012	Convenor	Dept. of EEE, NIT-T
Workshop on “Electronic Circuit Design techniques”	National	28-09-2007 & 29-09-2007	Convenor	Dept. of EEE, NIT-T
Two Day training programme on “ARM processor based development system”	National	23-05-2007 & 24-05-2007	Convenor	Dept. of Electronics Engg., MIT, Chennai
Short Evening course on “Computer Aided design of Electronic Circuits”	National	21-01-2004 to 28-01-2004	Convenor	Dept. of ECE, Bannariamman Institute of Technology, Sathyamangalam.

**18. Invited Talks delivered**

Topic	Date	Inviting Organization
ASIC Design Flow	September, 2007	IETE student Chapter, MIT, Anna University, Chennai
Digital System Design Using VHDL & VLSI implementation of Embedded Systems	December 2008	AICTE – ISTE sponsored course, LMCST, Trivandrum
Embedded Systems design using VLSI	February, 2009	PSNA College of Engineering, Dindigul
VLSI in Embedded and robotics Applications	April, 2009	PSNA College of Engineering, Dindigul
VLSI for Embedded Control Systems	January 2010	Veltech engineering College, Chennai
Digital system design using VHDL and Embedded design	March 2010	Mookambigai College of Engineering, Kalamavur

**19. Membership of Learned Societies**

Type of Membership (Ordinary Member/ Honorary Member / Life Member)	Organization	Membership No. with date
Ordinary Member	IEEE	
Life Member	ISTE	
Life Member	IETE	
Life Member	IE	AM097335-4
Senior Member	IACSIT	



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**20. Academic Foreign Visits**

Country	Duration of Visit	Programme
Mauritius	September 2013	AFRICON 2013
Singapore	30 <sup>th</sup> May 2013 to 10 <sup>th</sup> June 2013	Study programme under TEQIP-II
Indonesia	November 2011	TENCON 2011
Sweden	2010 & 2011	Post-doctoral research

**21. Publications**

**(A) Refereed Research Journals:**

Author(s)	Title of Paper	Journal	Vol. (No.)	Page Nos.	Year
R. Ramya and S. Moorthi	Modeling and Simulation of Frequency Response Masking FIR Filter Bank using Approximate Multiplier for Hearing Aid Application	Advances in System Science and Applications, an International Journal	18	74-91	2018
Nikhil K.A., P. Bharat Chandra, M.R. Jawahar, S. Moorthi, M.P. Selvan and N. Kumaresan	FPGA-based closed-loop monitoring and control of doubly fed induction generator with single inverter and battery for wind energy conversion	Australian Journal of Electrical and Electronics Engineering	In Press ( <a href="https://doi.org/10.1080/1448837X.2018.1548993">https://doi.org/10.1080/1448837X.2018.1548993</a> )		2018
Dheepanchakkravarthy, A., Selvan, M.P. and S. Moorthi	Alleviation of Current Quality Issues Caused by Electric Arc Furnace Load in Power Distribution System Using 3-Phase Four-Leg DSTATCOM	Journal of the Institution of Engineers (India) - Series B	100	9-22	2019
Dheepanchakkravarthy, A., Akhil, S., Venkatraman, K., Selvan, M.P. and S. Moorthi	Performance Analysis of FPGA Controlled Four-leg DSTATCOM for Multifarious Load Compensation in Electric Distribution System	Engineering Science and Technology, an International Journal	21	692-703	2018
Dheepanchakkravarthy, A., Venkatraman, K., Selvan, M.P., Moorthi, S. and M. Venkatakirthiga	Capability Evaluation of Four-leg DSTATCOM for Compensating Multifarious Loads	Australian Journal of Electrical and Electronics Engineering	13	229-243	2016
Venkatraman, K., Selvan, M.P., Moorthi, S., Raja, P. & Deepa Kurup	Predictive Current Control of DSTATCOM for VAR Compensation of Grid Connected Wind Farms	Journal of Renewable and Sustainable Energy	1	1941-7012	2017
Venkatraman, K., Moorthi, S., Selvan, M.P. and Raja, P	A Comprehensive Embedded Solution for Data Acquisition and Communication using FPGA	Journal of Applied Research and Technology, Elsevier	15	45-53	2017
Venkatraman, K., Moorthi, S. and Selvan, M.P	Modelling and Control of Transformer-less Universal Power Quality Conditioner (TUnPQC): An Effective Solution for Power Quality Enhancement in Distribution System	Journal of Control, Automation and Electrical Systems, Springer	28	123-134	2016

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Venkatraman, K., Dastagiri Reddy, B., Selvan, M.P., Moorthi, S., Kumaresan, N. and Ammasaigounden, N	Online Condition Monitoring and Power Management System for Standalone Micro-grid using FPGAs	IET Generation, Transmission and Distribution	10	3875-3884	2016
Venkatraman, K., Selvan, M.P. and Moorthi, S	Predictive Current Control of Distribution Static Compensator for Load Compensation in Distribution System	IET Generation, Transmission and Distribution	10	2410-2423	2016
Venkatraman, K., Moorthi, S., Selvan, M.P., Raja, P. and Deepa Kurup	Performance Evaluation of FPGA-Controlled DSTATCOM for Load Compensation	Arabian Journal of Science and Engineering	41	3355-3367	2016
Dastagiri Reddy, B.; Selvan, M.P.; Moorthi, S	Design, Operation and Control of S3 Inverter for Single-Phase Micro-Grid Applications	IEEE Transactions on Industrial Electronics	62	5569 – 5577	2015
Dastagiri Reddy, B.; Anish, N.K.; Selvan, M.P.; Moorthi, S	Embedded Control of n-level DC-DC-AC Inverter	IEEE Transactions on Power Electronics	30	3703 - 3711	2015
Dastagiri Reddy, B.; Selvan, M.P.; Moorthi, S	A Simplified Embedded Control Scheme for Two-Stage Multistring Off-Grid Inverter	IET Power Electronics	07	2954 - 2963	2014
Vasumathi B., S. Moorthi	Improved neural network algorithms with time-varying Widrow-Hoff learning rule for harmonic estimation	Australian Journal of Electrical and Electronics Engineering	09	377-385	2012
Naizath Amirtharaj J., K. Siddharth, S. Moorthi, N. Kumaresan	Voltage control Scheme for a Single Phase Voltage Source Inverter (VSI) using FPGA	Australian Journal of Electrical and Electronics Engineering	09	197-207	2012
B. Vasumathi and S. Moorthi	Implementation of hybrid ANN–PSO algorithm on FPGA for harmonic estimation	Engineering Applications of Artificial Intelligence	25	476-483	2012
B. Vasumathi and S. Moorthi	Modified ADALINE algorithm for harmonic estimation and Selective Harmonic Elimination in inverters	International Journal of Electronics, Taylor and Francis	98	1517-1529	2011
Moorthi S., Meganathan D., Janarthanan D., Praveen Kumar P. and Raja Paul Perinbam J	Low Jitter ADPLL based Clock Generator for High Speed SoC Applications’	International Journal of Electronics and Communication Engineering	02	1189-1193	2009
D. Meganathan, Amrith Sukumaran, M. M. Dinesh Babu, S. Moorthi, R. Deepalakshmi,	A systematic design approach for low-power 10-bit 100 MS/s pipelined ADC	Microelectronics Journal, Elsevier	40	1417-1435	2009
D. Meganathan, S. Moorthi, Amrith Sukumaran, M. M. Dinesh Babu, and J. Raja Paul Perinbam	A 52.6 mW 10-bit, 100 MS/s Pipelined CMOS Analog-To-Digital Converter	Journal of Low Power Electronics	4	208-227	2008

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Moorthi S., Meganathan D., Ammasai Gounden N. and Raja paul perinbam	A Wide Bandwidth Fast Locking CMOS Delay Locked Loop For System On-chip Applications	International Journal of Systemics, Cybernetics and Informatics		33-36	2008
Moorthi S., Sindhuja S., Anuradha R. and Raja Paul Perinbam J	Low Jitter Analog Phase Locked Loop Using Differential VCO	International Journal of Systemics, Cybernetics and Informatics,		66-68	2008
S. Aditya and S. Moorthi,	Wide Tuning Low Noise OTA based Current Controlled Ring Oscillator to suit PLL Applications	National Journal of Technology	10	66-72	2014

**(B) Conferences/Workshops/Symposia Proceedings**

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page Nos.	Venue	Year
P. Dinesh, K. Kumar Teja, Shashank Singh, Selvan M.P., and S. Moorthi	FPGA Based SoC Estimator and Constant Current Charging/Discharging Controller for Lead-Acid Battery	5th IEEE India Council International Conference		Coimbatore, Tamilnadu, India	2018
R. Ramya and S. Moorthi	Design and Implementation of Accuracy Configurable Multi-Precision Multiplier Architecture for Signal Processing Applications	IEEE Recent Advances in Intelligent Computational Systems (RAICS)		Trivandrum, Kerala, India	2018
S.P. Jayasri, N. Vyshnavi, S. Moorthi,	Wireless monitoring, control and automation in pulp and paper industry	IEEE- INDICON 2017	15 – 17	IIT Roorkee, INDIA	2017
Jawahar, M.R., Ajay Kumar, V., Moorthi,S. and Selvan, M.P	FPGA based SHEPWM Switching Scheme for Single Phase Cascaded H-Bridge Multi-Level Inverter	FPGA based SHEPWM Switching Scheme for Single Phase Cascaded H-Bridge Multi-Level Inverter	3257-3261	Delhi Technological University, Delhi INDIA	2016
Hrishkesan, V.M., Venkatraman, K., Selvan, M.P. and Moorthi, S	Application of D-STATCOM in SCIG based Wind-farms during Normal and Abnormal Grid Conditions	National Power Systems Conference (NPSC2014)	1-5	IIT-Guwahati, INDIA	2014
S. Aditya and S. Moorthi	A Low Jitter Wide Tuning range Phase Locked Loop with Low Power Consumption in 180nm CMOS Technology	IEEE-Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics	228-232	Visakhapatnam, India	2013

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S. Aditya and S. Moorthi,	Wide Tuning Low Noise OTA based Current Controlled Ring Oscillator to suit PLL Applications	International Conference on Intelligent and Efficient Electrical Systems (ICIEES)		PSG College of Technology, Coimbatore.	2013
Anish NK, Murali T., Dastagiri Reddy B., S. Moorthi, Selvan M. P	FPGA Based Control Scheme for a Single-Stage Grid-Connected Solar Photovoltaic System	AFRICON 2013	1-4	Pointe-Aux-Piments, Mauritius	2013
Anish NK, Kowshick B., S. Moorthi,	Ethernet based Industry automation using FPGA	AFRICON 2013	1-4	Pointe-Aux-Piments, Mauritius	2013
Anish NK, S. Moorthi,	Intelligent Location identification and passenger – alert system in Indian Railways using GPS receiver	AFRICON 2013	1-5	Pointe-Aux-Piments, Mauritius	2013
Venkatraman K, T.R. Aashish, Selvan MP, S. Moorthi	Real Time Object Tracking in a Video Stream using FPGA	IEEE- INDICON 2012	167-170	Kochi, India	2012
Gauri Shankar Singh, Devesh Singh, S. Moorthi	Low power Low Jitter Phase Locked Loop for High Speed Clock Generation	IEEE-PRIMEASIA 2012	192-196	Hyderabad, India	2012
Anish NK, Deepak Krishnan, S. Moorthi, Selvan MP	FPGA Based Microstepping Scheme for Stepper Motor in Space-Based Solar Power Systems	Seventh International Conference on Industrial and Information Systems, IEEE-ICIIS 2012	1-5	IIT Madras, Chennai, INDIA,	2012
Venkatraman, K., Selvan, M.P. and Moorthi, S	Investigation on Series Active Filter with Small Energy Source for DC Voltage Control	Springer International Conference on Control, Communication and Power Engineering	454-459	Bangalore, INDIA	2012
Anish NK, Deepak Krishnan, S. Moorthi, Selvan MP	FPGA Based Microstepping Scheme for a Stepper Motor in Solar tracking Applications	International Conference on Renewable Energy Utilization 2012 - ICREU 2012	1-5	CIT Coimbatore, INDIA	2012
Venkatraman, K., Selvan, M.P. and Moorthi, S	Performance Analysis of Series Active Filter under Non Sinusoidal Supply and Non Linear Loading	IEEE International Conference on Power and Energy Systems (ICPS - 2011)	1-6	, IIT Madras Chennai, INDIA	2011
Venkatraman, K., Selvan, M.P. and Moorthi, S.,	Performance of Series Active Filter in Low Voltage Distribution System with Non Linear Loads	IEEE International Conference INDICON-2011	1-6	Hyderabad, INDIA	2011

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Vivek R., Nalamwar Sanchit Gopal, Rahul Ashok, S. Moorthi	FPGA based DC Servo motor Control for Remote Replication of Movements of a Surgical Arm	IEEE-TENCON 2011	671-675	Indonesia	2011
S. Moorthi, D. Meganathan, M. Shankar, R. Sridhar, J. Raja paul perinbam	A Low- Jitter Phase- Locked Loop Architecture for Clock Generation in Analog to Digital Converters	IEEE Conference on Recent Advances in Intelligent Computing	81-84	Trivandrum	2011
S. Moorthi, D. Meganathan, N. Krishnaprasad, J. Raja paul perinbam	A Novel 14 ~ 170 MHz All Digital Delay Locked Loop with Ultra Fast Locking for SoC Applications	IEEE Conference on Recent Advances in Intelligent Computing	76-80	Trivandrum	2011
Krishna Chandran Vinay, Shyam H N, S. Rishi, S. Moorthi	FPGA Based Implementation of Variable-Voltage Variable-Frequency Controller for a Three Phase Induction Motor	International IEEE Conference on Process Automation, Control and Computing	1-6	CIT, Coimbatore	2011
S. Senthil Kumar, D. Ganesh, P. Raja, S. Moorthi	Voltage Controller in Photo-Voltaic system without Battery storage for Stand-alone applications	IEEE International Conference on Electrical, Control and Computer Engineering	269-274	Pahang, Malaysia	2011
B. Vasumathi, S. Moorthi	Harmonic Estimation Using Modified ADALINE Algorithm with Time-Variant Widrow – Hoff (TVWH) Learning Rule	IEEE International Symposium on Computers and Informatics	113-118	Kuala Lumpur, Malaysia	2011
M.A. Tajammul, A. Hemani, M.A. Shami, S. Moorthi	NoC Based Distributed Partitionable Memory System for a Coarse Grain Reconfigurable Architecture	24th International Conference System on VLSI Design	1-6	Tampere, Finland	2011
Subanna ch, Naizath Amirtharaj , Siddharth K and Moorthi S	FPGA based Closed loop Torque Control Scheme for a Stepper Motor	7th International Conference on Trends in Industrial Measurement and Automation		Chennai, India	2011
M.A. Tajammul, A. Hemani, M.A. Shami, S. Moorthi	A NoC Based Distributed Memory Architecture with Programmable and Partitionable Capabilities	IEEE NorChip Conference	1-6	Finland	2010
Moorthi S., Meganathan D., Janarthanan D., Praveen Kumar P. and Raja Paul Perinbam J	Low Jitter ADPLL based Clock Generator for High Speed SoC Applications	International Conference on Computer, Electrical and Systems Science and Engineering		Singapore	2008

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Moorthi S., Aravindhan M. and Raja Paul Perinbam J.	PLL Based High Frequency Clock Generator for High Speed Digital Communication Systems	IEEE-International Conference on Signal Processing, Communication and Networking	442 - 445	Chennai, INDIA	2008
Moorthi S., Pavitra K. and Raja Paul Perinbam J.	A Novel All Digital Phase Locked Loop for Phase Tracking in GPS Receivers	10th IEEE VLSI Design and Test Symposium	229-237	Goa.	2006
Moorthi S., Lokesh S. and Raja Paul Perinbam J	Design and Implementation Of Clock Signal Recovery Circuit For NRZI Coded Data Using FPGA	International Conference on Resource Utilization and Intelligent Systems	1247- 1251	Kongu Engg. College, Erode.	2006

**(C) Books & Monographs**

Author(s)	Title of Book/Monograph	Name of Publishers	Year of Publication	ISSN/ISBN Number