

**Department of Electronics & Communication Engineering,  
National Institute of Technology, Tiruchirappalli.**

27 SEP 2018

**ADVERTISEMENT FOR THE TEMPORARY POST OF JUNIOR RESEARCH FELLOW  
IN ECE DEPT**

Applications are invited from B.Tech/BE graduates having valid GATE Score with **ECE/ICE/E&I specialization** for the temporary post of Junior Research Fellow in the R&D project on **Adaptive Telemetry System for Launch vehicles-Demonstration of Proof of Concept**. Applicants who have completed PG degree in VLSI/Embedded systems/ Instrumentation & Control/Control systems/ any other relevant area related to the project with ECE/ICE/E&I as UG specialization may also apply.

- **Candidates having only B.E/B.Tech degree must have a valid GATE Score**
- The selected staff would be required to work on the design of Adaptive Telemetry blocks
- The applicants are expected to have a sound knowledge of digital circuit design technique and would be required to work on FPGA boards and VLSI CAD tools
- Salary: Rs.25,000/= per month(consolidated)+ 16% HRA (if eligible)
- Duration of appointment: 30 months
- Last date & time for submission of application : 26<sup>th</sup> Oct 2018 5:00 PM
- Shortlisted candidates for written test would be intimated through email by 31<sup>st</sup> Oct 2018 5 PM

The application should be submitted by filling in the google form

<https://goo.gl/forms/WNG496uCTt2cH4X03>

For any query related to this advt. send email to

**wsdlab.nitt@gmail.com** with subject as: JRF advt.

Coordinator,

RESPOND Project on Adaptive Telemetry System for Launch vehicles-Demonstration of Proof of Concept

ECE Dept.,

NIT, Trichy 620015