



## Department of Electrical and Electronics Engineering National Institute of Technology - Tiruchirappalli

Tanjore Main Road, National Highway 67, Near BHEL Trichy, Tiruchirappalli, Tamil Nadu 620015

Date: 25/10/2019

### Call for the position of Junior Research Fellow (JRF) for the DST Project on

### “Indo-Danish collaboration for data-driven control and optimization for a highly Efficient Distribution Grid (ID-EDGe)”

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the *Department of Science and Technology, Govt. of India*, funded international research and development Project on “Indo-Danish collaboration for data-driven control and optimization for a highly Efficient Distribution Grid (ID-EDGe)”.

#### Essential Qualification

M.Tech. /M.E. (with GATE qualification) in Power System/ Power Engineering/ Energy Systems / Power System Automation / Power Plant Engineering / Energy Management.

#### Desirable Qualification

Experience in software programming with technical software/tools (MATLAB/SIMULINK, PSCAD, Python, C and C++) for simulation studies.

#### Fellowship

JRF and Rs. 31,000/- plus 8% HRA for the first two years, and Rs. 35,000/- plus 8% HRA for the third year

#### Duration

3 Years-At the beginning, assignment will be for one year, which can later be extended every six months up to the completion of project.

#### How to apply

Interested candidates are requested to submit application in proper format (available with this notification) and detailed curriculum vitae through email and hard copy of application should reach the Co-Principal Investigator, by **Nov 20, 2019**.

Shortlisted candidates will be intimated through email by **Nov 22, 2019**.

Shortlisted candidates will have to report for written test/interview on Nov 28, 2019 (tentative) (2:30 PM) in Department of Electrical and Electronics Engineering, NIT Trichy.

#### Note

1. No TA/DA will be paid for appearing written test/interview.
2. Candidate selected for this position can also apply for the regular PhD program provided they satisfy the eligibility criteria for PhD program.
3. Decision of Director NIT Trichy will be the final in all respect.

#### Correspondence address: Co-PI

Dr. Sishaj P Simon,

Associate Professor

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**OFFICE OF THE DEAN (R & C), NIT, TIRUCHI  
RECRUITMENT OF JUNIOR RESEARCH FELLOW**

<p style="text-align: center;">For Office Use only</p> <p>Serial Number allotted to the Applicant .....</p> <p style="text-align: center;">Signature of the Application Screening Committee Chairman</p>	<p style="text-align: center;">Paste here your recent passport size colour Photograph</p>
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**APPLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW**

1. Name of the Candidate in full					
2. Nationality					
3. Mother Tongue					
4. Date of Birth	Date	Month	Year		
5. Sex	Male / Female				
6. Marital Status	Married / Single				
7. Category      SC / ST / OBC / General	Please specify				
8. Address for communication with Mobile number and E Mail ID					
9. Project Title					
10. Name of the Post applied for					
11. Advertisement Number with Date					
Academic Career : Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPF from HSC onwards					
Name of Exam	School / University	Board/ University	Broad subjects of study	Year of Passing	% of marks / GCPA
S.S.L.C					
H.S.C					
Graduation					
Post- Graduation					
Any other					

Enclose self-attested Xerox copies

12. GATE Score .....Subject  
.....Year.....

13. NET Score  
.....Subject.....Year.....

14. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	To	

14. Previous Research work and publication, if any (enclose copies of published or unpublished work)

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15. Proof of past study as a sponsored candidate, or other contractual obligation, if any:

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16. Reference

Name	Address	Phone & E Mail ID

17. I do not have a relative among Faculty and Staff of the NIT, Tiruchy.

18. Declaration: I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:

Full signature of the Applicant

List of Enclosures: (1)  
(2)  
(3)  
(4)