

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY, TRICHY-15**

21 DEC 2011

**ADVERTISEMENT**

Applications are invited for the following temporary posts in the R&D project on **VLSI Implementation of Wireless Sensor networks** funded by Department of Information Technology, New Delhi :

1. Research Associate

*Essential Qualifications:*

I Class M.E./M.Tech in VLSI system/ Microelectronics/communication system with at least 1 year experience on VLSI CAD tools .

Consolidated Salary: Rs.20,000-22,000/- per month.

Last date for submission of the completed application : 10 JAN 2012

2. Junior Research Fellow

*Essential Qualifications:*

I Class B.E./B.Tech in any circuit branch with valid GATE Score .

Desirable: Exposure to HDL/ VLSI CAD tools

Consolidated Salary : Rs.9,000/- per month.

Last date for submission of the completed application : 10 JAN 2012

For more details and application format see below.

**HOD/ECE**

**Advertisement for the post of Research associate/Junior research fellow for R&D project on VLSI Implementation of Wireless Sensor networks funded by Department of Information Technology , New Delhi :**

**Information to the candidates**

**No. of posts: 1. Research associate : Two                      2. Junior research fellow : One**

**Tenure:**

The post is purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended upto a maximum of additional 1or 2 years depending upon the performance. The tenure will not be extended beyond 3 years.

**Qualification and Salary for Research associate:**

*Essential Qualifications:*

I Class M.E./M.Tech in VLSI system/ Microelectronics/communication system with at least 1 year experience on VLSI CAD tools .

The initial salary may be fixed from Rs.20, 000 to Rs. 22,000 (consolidated) depending upon the previous experience.

**Qualification and Salary for Junior Research Fellow**

*Essential Qualifications:*

I Class B.E./B.Tech in any circuit branch with valid GATE Score .

Desirable: Exposure to HDL/ VLSI CAD tools

Consolidated Salary : Rs.9,000/- per month.

**Registration for Part time degrees.:**

**Research associate**, if found suitable, can register for **part time Ph.D.**

**Junior research fellow**, if found suitable, can register **for part time M.S.**

Last date for submission of the completed application : 10 JAN 2012

The application should be sent to

**The Coordinator, DIT project ,  
Department of ECE,  
NIT, Tiruchirappalli – 620 015.**

Application form and other details can be had from [www.nitt.edu](http://www.nitt.edu).

Superscribe the cover as “Application for the post of temporary staff in DIT project ”



**NATIONAL INSTITUTE OF TECHNOLOGY  
TIRUCHIRAPPALLI- 620 015**

APPLICATION FOR THE TEMPORARY POST OF RESEARCH ASSOCIATE/  
JUNIOR RESEARCH FELLOW IN DIT PROJECT.

[Please tick the relevant post(s)]

Recent  
Passport size  
photograph to  
be affixed.

1.	Name in Full (Capital Letters) (as in SSLC Certificate)				
2.	Date of Birth		Day	Month	Year
	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
3.	Address to which Communications should be sent (Also furnish email, fax, telephone number, if any)				
4.	Permanent Home Address				
5.	Name and Address of Father / Husband				
6. Details of University Education from SSLC onwards					
Degree	Discipline	University	Class/Division	% Marks	Year of Passing

7 Professional Experience in chronological order upto the present post				
Organisation	Designation	From	To	Scale of Pay
8 List of papers published/ awards & distinctions won				
9 List of programming languages and VLSI CAD tools in which you have experience				
10 List training programmes attended				
11 List of subjects taught (if any)				

