

## Schedule for Probe 2010

### Day 1 12<sup>th</sup> March

- 8:30 - 9:30 Inauguration
- 9:30 – 11:00 Guest Lectures by Dr. Surendra Pal & Mr. Mahadevan ISRO
- 11:30 – 12:30 Workshop (theory session)  
DSP workshop for Batches 1 & 2  
Radio circuitry for batch 1
- 13:00-14:00 Verilogic (digital design event) - on the spot Prelims
- 14:00-17:00 Magnum Opus (Paper Presentation)
- 15:00-16:30 DSP Workshop for Batch I(Practical session)
- 16:30-18:00 DSP Workshop for Batch II(Practical session)
- 16:00-18:00 Radio circuitry workshop for Batch I(Practical session)
- 17:00-19:00 Verilogic finals

### Day 2 13<sup>th</sup> March

- 8:30-10:30 DSP Workshop for Batch 1 (Practical session 2)
- 9:30-10:30 Radio circuitry for batch 2 (Theory Session)
- 11:00-13:00 Bob the builder (embedded system design event)
- 10:00-14:00 Techie Pirate Loot (Online Treasure hunt)
- 10:30-12:30 Workshop (practical session)  
Radio circuitry for batch 2
- 11:30-13:30 Workshop (Practical session)  
DSP workshop for Batch 2
- 15:30-16:30 Guest Lecture by Dr. Shanthi Pavan (IIT Madras)
- 16:30-17:00 Valediction