Curriculum Vitae



Brief Profile: I completed my PhD from IIT Roorkee in 2016 in nano-scale device simulation and modelling. Before joining this institute I have worked with BML Munjal UniversityGurugram, Haryana, for 3 years. I have total 5.5 years of teaching experience.

My research interest is nano devices and their applications for digital/analog or sensor domain. Interested candidates (UG/PG/PhD) can send e-mail on menka@nitt.edu.

1. Name: Menka (Female)

2. Designation: Assistant Professor

3. Office Address: Room No. 324, SJB, Department of ECE, NIT Trichy

4. Telephone (Direct) (Optional):

Telephone: Extn (Optional):

Mobile (Optional): 9416794011

5. Email (Primary): menka@nitt.edu Email (Secondary) : menka.er@gmail.com

6. Field(s) of Specialization:Microelectronics, VLSI, Semiconductor device deisgn, fabrication and applications

7. Employment Profile

Job Title	Employer	From	То
Assistant Professor	BML Munjal University	27.3.2015	10.4.2018
Assistant Professor	SMEC Neemrana, RTU	27.8.2009	29.12.2010
Lecturer	DAV CET Kanina, MDU	18.8.2008	29.5.2009
Trainee	Texas Instruments	1.10.2007	31.3.2008

Pvt. Limited,	
Bangalore	

8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
PhD	IIT Roorkee	2016		Tunnel FET Device Simulation and modelling
M.Tech	MNIT Jaipur	2008	1st	VLSI Design
B.Tech.	Govt. Engg. College Ajmer/ Rajasthan University	2006	1st	Electronics and Communication Engineering
10+2	Board of secondary Education Rajasthan	2002	1st	Physics, Chemistry, Maths
10	Board of secondary Education Rajasthan	2000	1st	-

9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	To

10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	То

11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization

12. Fellowships

Year of Award	Name of the Fellowship	Awarding	From	То
		Organization	(Month/Year)	(Month/Year)
2011 to 2014	MHRD Fellowship	IIT Roorkee	Jan. 2011	Aug. 2014
2006 to 2008	MHRD Fellowship	MNIT	July 2006	June 2008
	_	Jaipur	-	

- 13. Details of Academic Work
 - (i) Curriculum Development
 - (ii) Courses taught at Postgraduate and Undergraduate levels
 - (iii)Projects guided at Postgraduate level
 - (iv)Other contribution(s)

14. Details of Major R&D Projects

Title of Project	Funding Aganay	Dura	ation	Status
Title of Project	Funding Agency	From	То	Ongoing/ Completed

15. Number of PhDs guided

Name of the PhD Scholar	Title of PhD Thesis	Role(Supervisor/ Co- Supervisor)	Year of Award
		•	

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date	Title of	Level of	Role (Participant/	Event Organized by	Venue
(s)	Activity	Event	Speaker/ Chairperson,		
		(International/	Paper presenter, Any		
		National/	other)		

	Local)		

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue

18. Invited Talks delivered

Topic	Date	Inviting Organization

19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life Member)	Organization	Membership No. with date
,		

20. Academic Foreign Visits

Country	Duration of Visit	Programme

21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
3	Two dimensional analytical modeling for asymmetric 3T and 4T double gate tunnel FET in sub- threshold region: Potential and electric field	Microelectronics Journal	44	1251- 1259	2013	1.231
3	Insights into channel potentials and electron quasi-Fermi potentials for DG tunnel FETs	Journal of Semiconductors	36	014005	2015	1.3
3	Super- threshold Semi Analytical channel potential model for DG Tunnel	Journal of Computational Electronics	14	566- 573	2015	0.994

FET			

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of	Title of the	Page	Conference	Venue	Year
	Abstract/	Proceedings	numbers	Theme		
	Paper					
3	A TCAD approach to evaluate channel electron density of double gate symmetric n-tunnel FET	India Conference (INDI- CON), 2012 Annual IEEE,	577-581	-	Kochi, India	2012

(C) Books & Monographs

Author(s)	Title of Book/Monograph	Name of	Year of	ISSN/ISBN
		Publishers	Publication	Number