

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

			Distinction	
B.E	Bharathidasan University	2001	First class with Distinction	ECE
H.S.L.C	Devangar Girls Higher Secondary School, Aruppukottai	1997	87.91	Maths, Physics chemistry, Biology
S.S.L.C	Devangar Girls Higher Secondary School, Aruppukottai	1995	92.8	

9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	To
Programme Officer	NSS Programme Officer for ECE, CSE wings	2007	Till Date
CoreTeam Member	MIS	2014	Till Date
Lab In charge	Electronic Circuits Laboratory	2007	2013
UG NBA Core Team Member	Department of ECE	2012	2014
PG NBA Core Team Member	Department Of ECE	2014	Till Date
Class coordinator	B.Tech ECE III- SEM , IV- Sem PG VLSI SYSTEM I -SEM	2007	Till Date

10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	To
Reviewer	IEEE Transaction on Very Large Scale Integration (IEEE TVLSI)	2013	
Project coordinator	Intel- FICE Internship	2014 winter vacation	
Faculty Guide	NIYANTRA Contest.	2015	

11. Awards, Associateships etc. NIL

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

Year of Award	Name of the Award	Awarding Organization

12. Fellowships NIL

Year of Award	Name of the Fellowship	Awarding Organization	From (Month/Year)	To (Month/Year)

13. Details of Academic Work

(i) Curriculum Development

Theory

ECPC13: Along with Basic device physics, Syllabus is upgraded with latest Display Devices. Like LED Display, OLED, and HDTV etc.,

Labs:

EC 211: To Upgrade EDC Lab NI ELVIS II Kits and LABVIEW Software are procured.

EC 314: VLSI System laboratory is upgraded as VLSI and Embedded System Laboratory. INTEL ATOM Processor and INTEL GALILEO KITS are included in the lab curriculum .Lab is also upgraded with New Personal Computers.

(ii) Courses taught at Postgraduate and Undergraduate levels

UG (ECE)

1. EC 207 Semiconductor Physics and Devices
2. EC 204 Transmission Lines and Waveguides
3. EC211 Devices and Networks Laboratory
4. EC 314 VLSI and Embedded System Design Laboratory

PG (VLSI SYSTEM)

1. EC 653 Basics of VLSI
2. EC 654 Electronic Design Automation (EDA) Tools

(iii) Projects guided at Postgraduate level

- Total projects Guided : 33

(iv) Other contribution(s)

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

14. Details of Major R&D Projects : NIL

Title of Project	Funding Agency	Duration		Status
		From	To	Ongoing/ Completed

15. Number of PhDs guided: NIL

Name of the PhD Scholar	Title of PhD Thesis	Role(Supervisor/ Co-Supervisor)	Year of Award

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National / Local)	Role (Participant/ Speaker/ Chairpers on, Paper presenter, Any other)	Event Organized by	Venue
One day 02-11-2007	workshop on “ WiMAX, 3G-Applications in Wireless Technologies “	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
Two days 18-12-2007 to 19-12-2007	workshop on “ Special Topics in Microwaves”	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
Two days 31-03-2008 to 01-04-2008	A series of lectures on “ CMOS-MEMS device Technology and the future”	Local	Participant	Dept. of ICE, NITT	NITT
One day 17-04-2008	workshop on “ Biologically Inspired Algorithms for Digital Signal Processing Applications”	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
Two days 14-05-2008 to 15-05-2008	workshop on “ Internet Technologies and Tools”	Local	Participant	Dept. of CSE, NITT under TEQIP	NITT
Two weeks 16-06-2008 to	staff development programme on “FUZZY LOGIC : THEORY AND	Local	Participant	Dept. of Mathematics &	NITT

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

28-06-2008	ENGINEERING PRACTICES”			Production Engineering, NITT MHRD – AICTE sponsored	
One week 21.08.2008 to 27.08.2008	Induction Training programme on “Instructional Design And Delivery System”	Local	Participant	NITTTR , Chennai MHRD sponsored	NITT
Two days 23.01.2009 to 24.01.2009	workshop on “Design of High Performance Systems Using FPGAs “	Local	Participant	Dept. of ECE, NITT	NITT
Two weeks 15-06-2009 to 27-06-2009	staff development programme on “Engineering Practices on Fuzzy Logic, Neural Networks and Hybrid Intelligent Systems”	Local	Participant	Dept. of Mathematics & Electrical & Electronics Engineering, NITT MHRD – AICTE sponsored	NITT
One week 28-06-2010 to 03-07-2010	staff development programme on “Recent Trends in Biomedical Systems”	Local	Participant	Dept. of ICE, NITT	NITT
One week 13-07-2010 to 17-07-2010	staff development programme on “Recent Trends in Microwave Integrated Circuits”	Local	Participant	Dept. of ECE, NITT	NITT
Two days 05-10.2012 to 06-10-2012	Faculty Oriented Programme on “Class Room Management & Communication”	Local	Participant	Dept.of Humanitics	Hotel Sangam, Tanjore.
Two weeks 04-06.2013 to 14-06-2013	ISTE workshop on “Analog Electronics”	National	Participant	IIT Kharagpur National mission on Education through ICT (MHRD)	NITT
Two days 26-08.2013 to 27-08-2013	workshop on “Recent Trends in Wireless Networks”	Local	Participant	Dept. of ECE, NITT, TEQIP-II sponsored	NITT
Two weeks 02-01.2014 to 12-01-2014	ISTE Workshop on “Signals and Systems”	National	Participant	IIT Kharagpur National mission on Education through ICT (MHRD)	NITT
Two days 28-04.2015 to 29-04-2015	“Conclave on Academic Reforms (CAR 2015)	Local	Participant	NITT	NITT

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convenor)

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
Workshop organized on Basics of Electronics and Communication Networking under TEQUIP for Rural School Teachers	Local	26-11-2007	coordinator	NITT

18. Invited Talks delivered: NIL

Topic	Date	Inviting Organization

19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life Member)	Organization	Membership No. with date
Member	IEEE	92236521
Life Member	ISTE	
Member	IAENG	119696

20. Academic Foreign Visits: NIL

Country	Duration of Visit	Programme

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
CK Midhun; J Joy; Kavitha, R.K.	"High-Speed Dynamic Asynchronous Pipeline: Self-Precharging Style"	<i>Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, IEEE TVLSI</i>	Volume: 22, Issue:10	2235 – 2239	2014	1.245
Absel, K.; Manuel, L.; Kavitha, R.K.	"Low-Power Dual Dynamic Node Pulsed Hybrid Flip-Flop Featuring Efficient Embedded Logic,"	Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, IEEE TVLSI	vol.21, no.9	1693-1704	2013	1.245

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numbers	Conference Theme	Venue	Year
Srinivas Jagarapu and R.K.Kavitha	A 2.4 GHz Low Noise Amplifier In 0.18um CMOS Technology.	<i>IJCA</i>	10–15	<i>International Conference on VLSI, Communications and Instrumentation (ICVCI)</i>	Kerala	2011
Manuel, L., Midhun, C. K., & Kavitha, R. K.	High speed dynamic asynchronous pipeline: Self-Controlled approach.	<i>IEEE</i>	592-596	<i>INDICON), 2012 Annual IEEE</i>	Bangalore	2012
Kavitha R.K and Saravanan	Dual Current Reuse Ultra Wide Band	<i>IEEE</i>		International Conference on Communication	Tamil Nadu	2013

National Institute of Technology, Tiruchirappalli:
 Performa for CV of Faculty/ Staff Members

Thangavel	Low Noise Amplifier			and Signal Processing (ICCSP '13)		
Mohammed Abbas, C.; Shukla, A.; Kavitha, R.K	Sub-1V ultra low-power voltage reference	<i>VLSI-SATA</i>	1-4	<i>VLSI Systems, Architecture, Technology and Applications</i>	Bangalore	2015

(C) Books & Monographs NIL

Author(s)	Title of Book/Monograph	Name of Publishers	Year of Publication	ISSN/ISBN Number