



Call for Junior Research Fellow (JRF) for ISRO-Respond Project

ADVERTISEMENT

Applications are invited from B.Tech./B.E. graduates with **ECE specialization** for the temporary post of junior research fellow in the ISRO respond project on **Design and development of 14-bit 10 MSPS 50mW SAR (Successive Approximation Register) ADC in 180nm CMOS technology**. Applicants who have completed PG degree with ECE as UG specialization may also apply. **The applicant should have qualified in GATE and fill up the details of GATE score and the year of acquiring the GATE score in the application form.**

- The selected staff would be required to work on the design and implementation of SAR ADC
- The applicants are expected to have a sound knowledge of analog circuit design technique and would be required to work on the VLSI CAD tools
- Salary: **Rs.31,000/= per month (consolidated)**
- Duration of appointment: **2 years** (The appointment would be made for 1 year initially and would be extended for another year if the performance is found to be satisfactory)
- Last date & time for submission of application : **6th DEC 2019 5:00 PM**
- Names of the shortlisted candidates would be put up in the web site: www.nitt.edu by **9th DEC 2019 5:00 PM.**
- **Written test/Interview : 16th DEC 2019 11:00 PM**
- **Venue : VLSI Laboratory, Silver Jubilee building (Dept. of ECE)**

The application should be submitted by filling in the google form

https://docs.google.com/forms/d/1mC1sO49dJK3eamf-qHmPdmqcB8Eig19Y4Ot0_5zWtj8/edit?usp=sharing

Coordinator,
ISRO respond project on **SAR ADC**
ECE Dept., NIT, Trichy 620015
Email: vlsilabnitt@gmail.com, Phone: 04312503300