

Department of Electronics and Communication Engineering
National Institute of Technology, Tiruchrappalli -620015

24 OCT 2017

The following candidates have been shortlisted for the written test/interview for the temporary post of laboratory engineer in SMDP-C2SD project in ECE dept, NIT, Trichy. These candidates are required to appear for the written test at 9:30 AM on 31.10.2017 in the VLSI laboratory in the 2nd floor of Silver Jubilee Building, NIT, Trichy. Short listed candidates would be required to appear for the interview in the afternoon. No TA/DA would be paid the test/interview. The candidates have to make their own arrangements for accommodation.

For additional queries, you may contact hodece@nitt.edu, 0431 2503300

HOD/ECE

Coordinator, SMDP-C2SD project

SI No	Name	Mail id
1.	RAJU KUMARASAMY	
2.	E. GOPALAKRISHNAN	krishna03vlsi@outlook.com
3.	PUTCHALA BHASKARA RAO	bhaskar.p41@gmail.com
4.	KRITIKA BHARDWAJ	kritika.bhardwaj117@gmail.com
5.	A.BALASUBRAMANIAN	
6.	HARSHA MARIO AKANO	akano474@gmail.com
7.	R. SARASWATHI	
8.	A. YUVARAJ	uvaraj2011@gmail.com
9.	S. GLADSON	gladson86@gmail.com
10.	R. MAHIMA	mahi.rmc7@gmail.com
11.	K. HARI KISHORE	
12.	P. ELANA ANDREWS	elanaann123@gmail.com
13.	P. KARTHIK	
14.	R. THIVYA SELVAM	
15.	R. GAYATHRI	gayathrime002@gmail.com
16.	H.A. HAJAN	hajanha@gmail.com
17.	V. THENMOZHI	vthenmozhimtech@gmail.com
18.	M. MUBARAK ALI	
19.	P. KRUTHIKAH	
20.	N. ARUN KUMAR	
21.	S. HEMA PRIYA	hemapriya1993@yahoo.com
22.	V. VIJAY	vvijay.veeramani@yahoo.com