<u>**OFFICE OF THE DEAN (R & C), NIT, TIRUCHIRAPPALLI**</u>

ADVERTISEMENT FOR THE POST OF JUNIOR RESEARCH FELLOW (JRF)

Date: 07-05-2024

Applications are invited from Indian Nationals only for Project Positions as per the details given below, for research project, under the principal investigator **Dr. Varun P. Gopi**, **Department of Electronics and Communication Engineering,** in **National Institute of Technology, Tiruchirappalli-15, Tamil Nadu.**

Title of the Project: **Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications**

1. Funding Agency for the Project: **MeitY (Chips to Startup (C2S) Programme)**
2. Project Positions and Number: **JUNIOR RESEARCH FELLOW (JRF)/PROJECT ASSOCIATE-I (PA-I)-02**
3. Qualifications: **Post Graduate Degree in Basic Science** OR Graduate / Post Graduate Degree in Professional Course selected through a process described through any one of the following:

   a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.

   b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR,. ICMR, IIT, IISc, IISER etc.

   Essential Requirements: **B. E/ B. Tech in Electronics & Communication Engineering** with minimum of 65% marks or 6.5 CGPA from a recognized University/ Institute with a qualified **GATE** score obtained in the last four years from the closing date of application *OR* **M.E/M.Tech in Embedded System/VLSI/Signal Processing** any other specialization in ECE selected through GATE with 65% marks or 6.5 CGPA having relevant research/ teaching experience as evidence from published papers in standard refereed journals.

   Skill Sets Required: Sound Knowledge of Verilog RTL Coding, Strong fundamentals in C/C++, MATLAB, AI algorithm implementation in C/Python, Multi-threading and Programming, Good knowledge in Linear algebra, Knowledge in Digital signal processing, adaptive signal processing & array signal processing, Hands on experience in FPGA Architecture and Programming, Hands on experience on AI based Micro-controllers and Microprocessors Programming and debugging, Hands on experience in IoT based electronic product design and development, Sensors and actuators used in healthcare applications and interfacing with microprocessors and micro-controllers.

4. Emoluments:
   JRF: 37,000+18% HRA (first 3 years) **& 42,000+18% HRA** for 4th & 5th year (**Valid Gate/NET score**)
   PA-I: 25000+18% HRA (first 3 years) and **28000+18% HRA** for 4th & 5th year (**No Gate/NET score**)
5. Duration: **5 Years** (extension based on performance review every 12 months)

6. Job Description: Data collection, Execution of the project, Report and Technical article preparation

A. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.

B. Eligible candidates may send the scanned copy of their duly filled applications (only in the prescribed format given below) along with the relevant supporting documents by email to: **micaihlab@gmail.com** with the subject marked as **"JRF/ PROJECT ASSOCIATE-I- Chips to Startup (C2S) Programme".**

C. Application should be furnished only in the prescribed format.

D. Experience including research, industrial field and others.

E. Attested copies of degree/ certificate and experience certificate.

F. Candidates shall bring along with them the original certificates at the time of interview for verification.

G. Please note that no TA/DA is admissible for attending the interview.

H. The last date and time of submission of duly filled in application **21.05.2024 at 5 P.M**.

I. The date of interview will be communicated to the shortlisted candidates by **E-mail.**

J. Age of the applicant should be less than **35 years** as on the closing date of application

The candidate selected as Junior Research Fellow, will be permitted to register for PhD/MS-PhD Programme as per NITT rules and regulations

(Dr. Varun P. Gopi)

Principal Investigator

OFFICE OF THE DEAN (R & C), NIT, TIRUCHI
RECRUITMENT OF TEMPORARY PROJECT STAFF --PRESCRIBED APPLICATION FOR THE
POST OF RESEARCH ASSOCIATE / JRF / SRF / TECHNICAL STAFF – SPECIMEN TEMPLATE

For Office Use only Serial Number allotted to the Applicant …………………………………………….. Signature of the Application Screening Committee Chairman	Paste here your recent passport size colour Photograph

APPLICATION FOR THE POST OF JRF

1. Name of the Candidate in full	
2. Nationality	
3. Mother Tongue	
4. Date of Birth	Date Month Year
5. Sex	Male / Female
6. Marital Status	Married / Single
7. Category SC / ST / OBC / General	Please specify
8. Address for communication with Mobile number and E Mail ID	
9. Project Title	
10. Name of the Post applied for	
11. Advertisement Number with Date	

Academic Career : Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPF from HSC onwards

Name of Exam	School / University	Board/ University	Broad subjects of study	Year of Passing	% of marks / GCPA
S.S.L.C					
H.S.C					
Graduation					
Post-Graduation					
Any other					

Enclose self-attested Xerox copies

12. GATE Score ……………………………Subject
……………………………………Year……………………………………………………………

13. NET Score
……………………………………Subject…………………………………………Year……………………………………
………………………………

14. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	To	

14. Previous Research work and publication, if any (enclose copies of published or unpublished work)

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15. Proof of past study as a sponsored candidate, or other contractual obligation, if any:

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16. Reference

Name	Address	Phone & E Mail ID

17. I do not have a relative among Faculty and Staff of the NIT, Tiruchy.

18. Declaration: I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date: Full signature of the Applicant

List of Enclosures: (1)
                     (2)
                     (3)
                     (4)