DEPARTMENT OF INSTRUMENTATION AND CONTROL ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI – 620 015 TEL: 0431 – 2503353, FAX: 0431 – 2500133

NITT/ICE/DST-SYST/JRF/2019

ADVERTISEMENT

Applications are invited from eligible candidates for position of Junior Research Fellow (JRF) post for DST-SYST sponsored research project titled "Development and Validation of an Affordable Handheld Device for diagnosing compressive neuropathy by two point discrimination test in diabetic subject" (Ref No: SP/YO/393/2018) in the Department of Instrumentation and Control Engineering, National Institute of Technology, Tiruchirappalli.

Temporary Position:	Junior Research Fellow (JRF)
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Number of post: One

Essential qualification: M.E/ M.Tech in Biomedical Engineering, Medical Electronics, Embedded system, Electronics & Instrumentation Engineering, Instrumentation & Control Engineering, Control & Instrumentation Engineering, Electronics & Communication Engineering with minimum of 65% marks or 7.5 CGPA from recognized University/ Institute (OR) B.E/ B.Tech in Instrumentation & Control Engineering, Biomedical

B.E/ B.Tech in Instrumentation & Control Engineering, Biomedical Engineering, Electronics & Communication Engineering with GATE qualification

Salary (Consolidate): Rs. 30,000 per month for initial 2 years & Rs. 33,600 per month for last year

Duration of Project: 3 Years

Age Limit: 32 years

- Job Description : 1. Purchase of equipment and components for the project
 - 2. Fabrication of sensory device by implementation of force concept
 - 3. Testing and validate the performance of device with Std device
 - 4. Relevant hardware and software Experience like Arduino and Raspberry pi, Matlab, LabView, Embedded C

Additional Information:Selected candidates will be encouraged to register Ph.D. as per
NIT Trichy regulations in the upcoming academic session.Important Note:Those who have attended interview earlier for this project are not
eligible to apply. In addition, VLSI specialization, Applied

Electronics people are not eligible to apply.

- Eligible candidates may send their applications on prescribed format along with Bio-Data and supporting documents by post/courier to Dr. R.Periyasamy, Assistant Professor, Department of Instrumentation and Control Engineering, National Institute of Technology, Tiruchirappalli – 620 015 with a copy to email: periyasamyr@nitt.edu with the subject marked as "JRF- DST SYST 2019". The last date for receipt of filled applications (hardcopy) on or before 12.04.2019. The date of interview will be communicated for the shortlisted candidates by post/E-mail/phone.
- 2. No T.A/D.A will be paid for attending the interview.

Dr. R. Periyasamy Principal Investigator

27th March 2019

OFFICE OF THE DEAN (R & C), NIT, TIRUCHI **RECRUITMENT OF TEMPORARY PROJECT STAFF -- APPLICATION** FOR THE POST OF JUNIOR RESEARCH FELLOW (JRF) Paste here your

recent passport

size color

Photograph

For Office Use only

Serial Number allotted to the Applicant

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Signature of the Application Screening Committee Chairman

APPLICATION FOR THE POST OF JRF

1. Name of the Ca	andidate in fu	ıll			
2. Nationality					
3. Mother Tongue	e				
4. Date of Birth			Date	Month	Year
5. Sex		Male / Female			
6. Marital Status			Married / Singl	e	
7. Category	SC / ST	/ OBC /	Please specify		
General					
8. Address for con	mmunication	with			
Mobile number a	nd E Mail II)			
9. Project Title					
10. Name of the F					
11. Advertisemen	t Number wi	th Date			
Academic Career	: Enclose ph	oto copies of t	the qualifying de	gree certificates	and mark
sheets/ grade cards showing the percentage of		of marks or CGP	F from HSC onv	vards	
Name of Exam	School /	Board/	Broad	Year of	% of marks /
	University	University	subjects of	Passing	GCPA
			study		
S.S.L.C					
H.S.C					
Graduation					
Post-Graduation					
Any other					

Enclose self-attested Xerox copies

12. Valid GATE Score

.....Year.....Year.....

13. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	То	

14. Area of specialization details:

Area of specialization	Relevant work or project work done	Duration	

15. Hardware and Software Proficiency:

16. Previous Research work and publication, if any (enclose copies of published or unpublished work)

17. References

Name	Address	Phone & E Mail ID

18. **Declaration:** I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:

Full signature of the Applicant

List of Enclosures:	(1)
	(2)