



NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI - 620 015
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

NOTICE INVITING QUOTATIONS

File No.	NITT/SEEDGRANT/PROJ.No.11/EEE/2571/SM/2024-2025/EP/01	Date:	11.03.2025
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To

(Supplier Address)

Sealed quotations are invited from reputed Authorized Dealers / manufacturers for the supply of the Items.

Name of the Item : Artix 7 FPGA Board with control Algorithm

Specification : (As per enclosed Schedule Annexure – I)

1. Quotation Reference No. :
NITT/SEEDGRANT/PROJ.No.11/EEE/2571/SM/2024-2025/LT/01

2. Last date and Time for receipt of quotation : **01.04.2025** at **2** PM

3. Date & Time of opening of Quotation : **01.04.2025** at **3** PM

4. EMD Amount : 2% MSME non-trading exempted

5. Validity (Days) : 60 Days

6. Address to which quotations are to be sent

The Director,

National Institute of Technology,

Tiruchirappalli – 620 015, Tamil Nadu, India

Kind attention to : Dr. Suman M, Assistant Prof., EEE, NITT

Phone : 9994121817

E-mail : sumanm@nitt.edu

7. Quotations should be submitted in the format given in Annexure – I and Annexure-II

8. The envelope should contain the following details:

<p style="text-align: center;">“QUOTATION AGAINST ENQUIRY (<u>Artix 7 FPGA Board with control Algorithm, NITT/SEEDGRANT/PROJ.No.11/EEE/2571/SM/2024-2025/EP/01</u>)</p> <p>The Deputy Registrar (R&C), Office of Dean (R&C), National Institute of Technology, Tiruchirappalli – 620 015, Tamil Nadu, India</p> <p><u>Kind attention to: Dr. Suman M, Assistant Prof., EEE, NITT</u></p> <p>Last date and Time for receipt of quotation : <u>01.04.2025</u> at <u>2 PM</u></p>

Terms and Condition

1.	The quotation must be in the format furnished by NIT Tiruchirappalli and should be free from corrections/erasures. In case there is any unavoidable correction it should be properly attested. If not the quotation will not be considered. Quotation written in pencil and incomplete will be rejected.
2.	Earnest Money Deposit (EMD) is to be submitted by way of Demand Draft drawn on any Nationalized bank in India in favor of “The Director, NIT, Tiruchirappalli” payable at Trichy. The bids submitted without EMD will be treated as non-responsive and will be rejected. EMD shall bear no interest. Bidder must fill the EMD returning Form (Annexure-III) and submit along with the quotation. EMD amount of 2% of estimate value is applicable, however UDYAM & NSIC registered Micro & small enterprises are exempted as per Govt.norms proof to be attached.
3.	You are invited to submit your most competitive quotation for the supply of goods according to the specifications and delivery terms as given. Bank guarantee submitted for EMD shall be valid for 45 days beyond bid validity period.
4.	Bidders may send the quotations in sealed covers with the quotation reference number and last date for receipt of quotations duly superscribed on the cover. Kind Attention to: as mentioned in the point No. 6 NB: Mention the company Contact Number / E-mail id on the cover.
5.	Quotation will be opened on due date at 3.00 PM at the Store and Purchase Section, NIT, Tiruchirappalli in presence of the tenderers or their representatives who may wish to be present. (Any change in the date, time and venue of the quotation opening will be informed to the bidders through telephone / E-mail)

6.	The National Institute of Technology, Tiruchirappalli reserves the right to accept or reject any quotations, and to cancel the bidding process, and reject all quotations at any time prior to the award of order without assigning any specific reason thereof.
7.	Manufacturer's name and country of origin of materials offered must be clearly specified. Printed brochures, Purchase preference is only for Micro & Small enterprises (MSE's) register in UDYAM Portal or NSIC as per Ministries policy for MSE's . Proof to be attached
8.	Samples must be submitted where specified along with the quotations. Samples must be carefully packed, sealed and labelled clearly with enquiry number, subject and sender's name for easy identification. Rejected samples will be returned at your cost if insisted
9.	All supplies are subject to inspection and approval before acceptance. Manufacturer / supplier warranty certificates and manufacturer / Government approved lab test certificate shall be furnished along with the supply, wherever applicable
10.	National Institute of Technology, Tiruchirappalli reserves the right to modify the quantity specified in this enquiry.
11.	Startup company exempted from prior turnover & prior experience (startup certificate registered with DIPP should be enclosed)
12.	The bidder has to submit the bids in sealed envelope, (separate for each tender). Further Bidder should not send clubbing many tenders in one envelope, in such case all the bids will be rejected.
13.	<p>Bid Price</p> <p>a. The contract shall be for the full quantity Bidders must quote for entire quantity. Each bidder shall submit only one quotation in Indian Rupee only.</p> <p>b. Post work orders & completion certificate should be submitted, wherever applicable.</p> <p>c. The rates quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.</p> <p>d. GST, packing, forwarding and delivering other allied items at the destination shall be included in the price. All such price components may be shown in the quotation. If there is no indication regarding above charges. It will be considered as inclusive of all charges.</p> <p>e. If any arithmetic mistake in total / GST calculation is observed, the same shall be corrected by the purchaser with an intimation to bidder</p> <p>f. In case the items in the enquiry are covered by any rate contract or any other state or central Government, it should be specified in your quotation and accepted contract rates should also be mentioned. It should be confirmed whether you could supply at the RC rates outside rate contract</p> <p>g. Quotations containing conditions like "subject to prior sale" may not be considered.</p> <p>h. Delivery period required for supplying the material should be invariably specified in the quotation</p> <p>i. Bids without quoting GST (unless exempted) will be treated as invalid & disqualified.</p> <p>j. If there is a discrepancy between unit price and total price, the unit price will be considered. If there is any mismatch between figure and word, the amount in word shall prevail.</p>
14.	Evaluation of quotations: Quotations will be evaluated item-wise or lump sum basis. The purchase committee will evaluate and compare the quotations determined to be substantially responsive i.e. (i) are properly signed; (ii) Conform to the terms & conditions and specifications; and (iii) price offered are competitive.

15.	<p>Award of contract</p> <p>a. The National Institute of Technology, Tiruchirappalli will award the Order for supply of Goods / Services to the bidder whose quotation has been determined to be substantially responsive, and who has offered the lowest evaluated quotation price.</p> <p>b. The Bidder should furnish the contract agreement and performance security within 15 days from the date of receipt of the order for supply of goods / services, failing which the order will be cancelled without further notice and awarded to next eligible bidder.</p> <p>c. Notwithstanding the above, National Institute of Technology, Tiruchirappalli reserves the right to accept or reject any quotations, and to cancel the bidding process, and reject all quotations at any time prior to the award of order without assigning any specific reason thereof.</p> <p>d. National Institute of Technology, Tiruchirappalli, prior to the expiration of the quotation validity period, will notify the bidder whose bid is accepted for the award of contract. The terms of accepted offer shall be incorporated in the purchase order.</p>
16.	<p>Warranty: (As mentioned in Annexure-I) shall be applicable to the supplied goods and installation work. Bidder should clearly indicate the arrangements for support and maintenance during the period for which the warranty shall be in force.</p>
17.	<p>Performance Security: Security deposit should be 3% of purchase order value in case of Performance Bank Guarantee should be valid for 60 days beyond the guarantee & warranty. The successful bidder need to submit performance security (<u>As mentioned in Annexure –I</u>) of purchase order value either in the form of bank guarantee or crossed demand draft drawn on any Nationalized bank in India in favor of “The Director, NIT, Tiruchirappalli” payable at Trichy. The bank guarantee / Demand Draft will be returned to the supplier after 60 days from date of successful completion of supply, installation, and the warranty period.</p>
18.	<p>Payment: 100% will be paid after Installation and satisfactory working/date of completion of service if the documents are in order. The bill should be raised in favor of “The Director, National Institute of Technology, Tiruchirappalli, Tamil Nadu, India.” with institute GST No. 33AAATN5491Q1ZZ.No advance will be provided to the supplier and installer.</p>
19.	<p>Liquidity damages</p> <p>If the bidder / supplier, after accepting the Purchase Order or supply of Goods / Services, fails to deliver any or all of the Goods or to perform Services within the period(s) specified in the Order, The National Institute of Technology, Tiruchirappalli shall impose penalty without assigning any reasons to the bidder / supplier a sum equivalent to 0.50 % of the total cost as indicated in the Purchase Order (which will be deemed as agreed price) for each week or part thereof of delay until actual delivery or performance is completed and such penal charges shall be limited to a maximum of 5% of the agreed price. Once the maximum is reached The National Institute of Technology, Tiruchirappalli, may proceed on its own to consider the termination / cancellation of the Order and may inform the bidder about the cancellation of the said purchase order. unless extension is obtained in writing from the office / Department on valid ground before expiry of delivery period</p>
20.	<p>If the deliveries are not maintained and due to that account Procuring Entity is forced to buy the material at your risk and cost from elsewhere, the loss or damage that may be sustained there by will be recovered from the defaulting supplier</p>
21.	<p>Dispute clause: Any dispute relating to the Enquiring /Tender of the indented item shall be under the Hon’ble Court having its jurisdiction over Tiruchirappalli only</p>

22.	Startup company exempted from Prior Turnover & Prior Experience (Startup certificate registered with DIPP should be enclosed)
23.	NIT Trichy has DSIR, so it's eligible for 5% in concession of GST and also exempted from Custom's Duty tax.
24.	Performance Bank guarantee should be valid for 60 days beyond the guarantee & warranty and the BG submitted for EMD shall be valid for 45 days beyond bid validity period.
25.	Bids submitted without EMD (unless exempted) will be treated as disqualified.
26.	The successful bidder should submit Security Deposit/PBG within 15 days from the date of placement of order. The EMD shall be returned only after receipt of SD. If the bidder fails to deliver the material, then the EMD/SD shall be forfeited.
27.	In LTE, bidder has to submit in sealed envelope (separate for each tender). Further Bidder should not send clubbing many tenders in one envelope, in such case all the bids will be rejected.

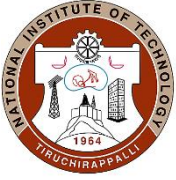
For any details / clarifications regarding could be obtained, Stores and Purchase Section from the National Institute of Technology, Tiruchirappalli on all working days during 10 AM to 5 PM.

For further detail kindly contact **Dr. Suman M., Assistant Professor, EEE, NITT,** sumanm@nitt.edu, **9994121817**

(NB: Mention the Contact Number / E-mail on the cover. Any change in the date, time and venue of the tender opening will be informed to the bidders through telephone / E-mail)

Enclosures:

Specifications of the equipment	Annexure - I
Price Format	Annexure - II
Bid Security Declaration form	Annexure – III
Bank Mandate Form	Annexure – IV



NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI - 620 015
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Annexure- I

Specifications of the Equipment

Ref: NITT/SEEDGRANT/PROJ.No.11/EEE/2571/SM/2024-2025/EP/01

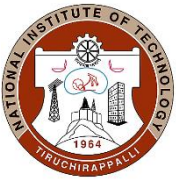
Date: 11.03.2025

Sl. No.	Specification of NIT-T	Specification of the Supplier	Reasons (if there is difference in specification)
1.	<p>Artix 7 XC7A200T FPGA</p> <ul style="list-style-type: none"> ○ 215360 logic cells, 13455360 BRAM bits ○ 740 DSP48 slices for computing DSP algorithms. ○ 10 clock management block for PLL and DCM. ○ USB 2.0 Hi-Speed communication ○ Onboard USB JTAG for programming ○ Isolated Power Input ○ Isolated USB UART ○ Bipolar SPI ADC <ul style="list-style-type: none"> ▪ No of ADC input: 16 Channels ▪ Resolution: 16 bit ▪ Sampling rate: 1MSPS ▪ Analog input range: $\pm 10V$(*BIPOLAR). ○ 20 pin headers for ADC input. ○ Unipolar SPI ADC <ul style="list-style-type: none"> ▪ No of ADC input : 8 Channels ▪ Resolution : 12 bit ▪ Sampling rate : 1MSPS ▪ Analog input range: 0 – 3.3V. ○ 10pin PTB connector for ADC input. ○ SPI DAC <ul style="list-style-type: none"> ▪ No of DAC output: 8 Channels ▪ Resolution: 12 bit ▪ 10 pin PTB connector for DAC output. ▪ 64Mb FLASH Memory ○ WIFI/Bluetooth for wireless communication ○ 8 Slide Switch for user input selection ○ 8 LED for user output selection ○ 2 Push Button ○ 50MHz Clock 		

	<ul style="list-style-type: none"> ○ 20x4 LCD interface ○ 100 nos of I/O ○ 40 pin headers for PWM Outputs and Capture Inputs - 64 I/O's (5v range) ○ 40 pin headers for GPIO line termination - 36 I/O's (3v3 range) ○ 4 No. of Push Button 		
2.	<p>Programming Mode: The Xilinx® Vivado® High-Level Synthesis (HLS) tool transforms a C specification into a register transfer level (RTL) implementation that you can synthesize into a Xilinx field programmable gate array (FPGA).</p>		
3.	<p>OS support: LCI-Artix- 7 controller should support Ubuntu /Linux/Windows based OS.</p>		
4.	<p>Example: Vivado based code and ISE Xilinx based example codes should work with available AC/DC Micro Grid systems and PMSM/AC/DC/BLDC/SynRM/BDFRM motors.</p>		
5.	<p>Control Algorithm for the below circuit for all the following languages/boards should be provided separately</p> <p>a) Artix 7 XC7A200T FPGA</p> <p>b) Verilog Coding</p> <p>c) VHDL Coding</p> <p>d) DSP Boards</p> <p>e) TMDSDOCK28035</p> <p>f) TMDSDOCK28379D Experimenter's Kit</p>		
6.	<p>The control algorithm for the following should be provided for the languages/boards given above</p> <p>a) PI Controller</p> <p>b) PID controller</p>		

	c) MPPT Controller (P & O Algorithm and incremental conductance)		
	d) abc to dq transformation		
	e) dq to abc transformation		
	f) SPWM generation		
	g) Phase locked loop		
7.	Installation (Required)		
8.	Warranty (in Month 36)		
9.	Comprehensive AMC Charge per year (NIL)		
10.	Delivery Period (8 to 12 Weeks)		
11.	Shipment terms	At NIT-T	
12.	Performance Security in 3%		

Signature & Seal of Vendor with Date



NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI - 620 015

DEPARTMENT OF _____

Annexure- II

PRICE BID FORMAT FOR BIDDERS

Tender No. & Date :

Bidder's Offer No. & Date :

S. No.	Description of item	Unit (Set / No)	QTY	Rate / Qty in Rs. (excluding GST)	GST in Rs.			Total Value + GST in Rs.
					SGST	CGST	IGST	
1.	Supply portion (The price indicated shall be exclusive of all accessories, spares etc. as given in the scope of supply)							
2.	Other accessories / spares etc. as given in scope of supply (Individual item-wise break-up price shall be attached as an annexure to this price bid format.)							
3.	Installation and Commissioning (extra, if any)							
4.	Packing & Forwarding charges (extra, if any)							
5.	Freight & Transit insurance charges, extra, if any							
6.	Total price (delivery, installation and commissioning at NIT-T)							
7.	Value of Annual Maintenance Contract							
8.	Net cost to be paid by NIT-T							

Signature & Seal of Vendor

Note: The price bid should be submitted only as per the above format. No row shall be left blank. Please indicate NA, in case the item is "Not Applicable". If this format is not used or any column is left blank, then the bid will be rejected.

NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI - 620 015

DEPARTMENT OF _____

(TO BE PRINTED IN LETTER PAD OF THE FIRM)

EMD Returning Form

To
The Director
National Institute of Technology,
Tiruchirappalli – 620 015

Sub: Returning EMD amount

Sir / Madam,

Our firm has participated in the tender / quotation enquiry No mentioned below and produced the EMD amount through DD, details of the DD are given below.

Tender / Quotation Reference No	
EMD amount	
DD Number	
DD issued Bank	
Date of DD	

It is requested to return the EMD amount to our firm after completion of the purchase to the below mentioned Bank account.

Account Name	
Bank Account Number	
IFSC code	
Bank	

Signature with Seal and Date

MANDATE FORM FOR ELECTRONIC FUND TRANSFER/RTGS TRANSFER

Date: / /

To

The Director,
National Institute of Technology,
Tiruchirappalli – 620 015, Tamil Nadu

Sub	:	Authorization for release of payment / dues from National Institute of Technology, Tiruchirappalli through Electronic Fund Transfer/RTGS Transfer.
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1. Name of the Party / Firm / Company / Institute :
2. Address of the Party :
3. City_____Pin Code_____
4. E-Mail_____Mobile No:_____
5. Permanent Account Number_____
6. Particulars of Bank:

Bank Name:		Branch Name:	
PIN Code:		Branch Code:	
IFS Code:(11 digit alpha numeric code)			
Account Type	Savings	Current	Cash Credit
Account Number:			

DECLARATION

I hereby declare that the particulars given above are correct and complete. If any transaction delayed and not effected for reasons of incomplete or incorrect information I shall not hold Director, National Institute of Technology Tiruchirappalli responsible. I also undertake to advise any change in the particulars of my account to facilitate updating of records for purpose of credit of amount through NEFT/RTGS Transfer.

Place:_____ Date: _____

Signature & Seal of the Authorized Signatory of the Party
